Refine Search

Search Results -

Terms	Documents
L8 and texture memory	42

Database:

US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
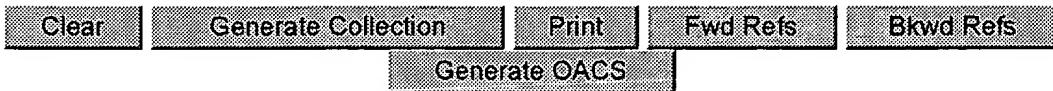IBM Technical Disclosure Bulletins

Search:

L9

[Refine Search]

[Recall Text ⬍] [Clear] [Interrupt]

Search History

DATE: Tuesday, January 20, 2004   Printable Copy   Create Case

Set Name side by side	Query	Hit Count	Set Name result set
	DB=PGPB,USPT,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ		
L9	L8 and texture memory	42	L9
L8	L7 and vertice$1	96	L8
L7	L6 and calculat$5	116	L7
L6	L5 and geometric	123	L6
L5	L3 and circuit	271	L5
L4	L3 and dircuit	0	L4
L3	L2 and frame buffer	374	L3
L2	L1 and rasteriz$5	554	L2
L1	float$3 adj point	18520	L1

END OF SEARCH HISTORY

Hit List

Search Results - Record(s) 11 through 20 of 42 returned.

☐ 11. Document ID: US 6567083 B1

Using default format because multiple data bases are involved.

```
L9: Entry 11 of 42                    File: USPT            May 20, 2003

US-PAT-NO: 6567083
DOCUMENT-IDENTIFIER: US 6567083 B1 '

TITLE: Method, system, and computer program product for providing illumination in
computer graphics shading and animation

DATE-ISSUED: May 20, 2003

INVENTOR-INFORMATION:
NAME                          CITY              STATE    ZIP CODE    COUNTRY
Baum; Daniel R.               Palo Alto         CA
Hanrahan; Patrick M.          Portola Valley    CA
Pharr; Matthew M.             Palo Alto         CA

US-CL-CURRENT: 345/426; 345/589
```

| Full | Title | Citation | Front | Review | Classification | Date | Reference | | | | Claims | KWIC | Draw De |

☐ 12. Document ID: US 6556197 B1

```
L9: Entry 12 of 42                    File: USPT            Apr 29, 2003

US-PAT-NO: 6556197
DOCUMENT-IDENTIFIER: US 6556197 B1

TITLE: High performance low cost video game system with coprocessor providing high
speed efficient 3D graphics and digital audio signal processing

DATE-ISSUED: April 29, 2003

INVENTOR-INFORMATION:
NAME                          CITY              STATE    ZIP CODE    COUNTRY
Van Hook; Timothy J.          Menlo Park        CA
Cheng; Howard H.              San Francisco     CA
DeLaurier; Anthony P.         Sunnyvale         CA
Gossett; Carroll P.           Mountain View     CA
```

Moore; Robert J.	Sunnyvale	CA	
Shepard; Stephen J.	Cupertino	CA	
Anderson; Harold S.	Morgan Hill	CA	
Princen; John	Sunnyvale	CA	
Doughty; Jeffrey C.	Palo Alto	CA	
Pooley; Nathan F.	Mountain View	CA	
Sheppard; Byron	Santa Cruz	CA	
Takeda; Genyo	Osaka		JP
Kato; Shuhei	Shiga		JP

US-CL-CURRENT: 345/419; 345/522

| Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw D |

☐ 13. Document ID: US 6504542 B1

L9: Entry 13 of 42   File: USPT   Jan 7, 2003

US-PAT-NO: 6504542
DOCUMENT-IDENTIFIER: US 6504542 B1

TITLE: Method, apparatus and article of manufacture for area rasterization using sense points

DATE-ISSUED: January 7, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Voorhies; Douglas A.	Menlo Park	CA		
Foskett; Nicholas J.	Mountain View	CA		

US-CL-CURRENT: 345/441

| Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw D |

☐ 14. Document ID: US 6476807 B1

L9: Entry 14 of 42   File: USPT   Nov 5, 2002

US-PAT-NO: 6476807
DOCUMENT-IDENTIFIER: US 6476807 B1

TITLE: Method and apparatus for performing conservative hidden surface removal in a graphics processor with deferred shading

DATE-ISSUED: November 5, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY

Duluk, Jr.; Jerome F.	Palo Alto	CA
Dodgen; Stephen L.	Boulder Creek	CA
Hessel; Richard E.	Pleasanton	CA
Fang; Emerson S.	Fremont	CA
Hsu; Hengwei	Fremont	CA
Redgrave; Jason R.	Mountain View	CA
Trivedi; Sushma S.	Sunnyvale	CA

US-CL-CURRENT: <u>345/421</u>; <u>345/422</u>, <u>345/506</u>, <u>345/539</u>, <u>345/582</u>, <u>345/589</u>

| Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw De |

□ 15.   Document ID: US 6342892 B1

L9: Entry 15 of 42   File: USPT   Jan 29, 2002

US-PAT-NO: 6342892
DOCUMENT-IDENTIFIER: US 6342892 B1

TITLE: Video game system and coprocessor for video game system

DATE-ISSUED: January 29, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Van Hook; Timothy J.	Menlo Park	CA		
Cheng; Howard H.	San Francisco	CA		
DeLaurier; Anthony P.	Sunnyvale	CA		
Gossett; Carroll P.	Mountain View	CA		
Moore; Robert J.	Sunnyvale	CA		
Shepard; Stephen J.	Cupertino	CA		
Anderson; Harold S.	Morgan Hill	CA		
Princen; John	Sunnyvale	CA		
Doughty; Jeffrey C.	Palo Alto	CA		
Pooley; Nathan F.	Mountain View	CA		
Sheppard; Byron	Santa Cruz	CA		
Takeda; Genyo	Osaka			JP
Kato; Shuhei	Shiga			JP

US-CL-CURRENT: <u>345/503</u>; <u>345/419</u>, <u>345/541</u>, <u>345/542</u>, <u>463/32</u>, <u>463/43</u>, <u>463/44</u>

| Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw De |

□ 16.   Document ID: US 6331856 B1

L9: Entry 16 of 42   File: USPT   Dec 18, 2001

US-PAT-NO: 6331856

DOCUMENT-IDENTIFIER: US 6331856 B1

TITLE: Video game system with coprocessor providing high speed efficient 3D
graphics and digital audio signal processing

DATE-ISSUED: December 18, 2001

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Van Hook; Timothy J.	Menlo Park	CA		
Cheng; Howard H.	San Francisco	CA		
DeLaurier; Anthony P.	Sunnyvale	CA		
Gossett; Carroll P.	Mountain View	CA		
Moore; Robert J.	Sunnyvale	CA		
Shepard; Stephen J.	Cupertino	CA		
Anderson; Harold S.	Morgan Hill	CA		
Princen; John	Sunnyvale	CA		
Doughty; Jeffrey C.	Palo Alto	CA		
Pooley; Nathan F.	Mountain View	CA		
Sheppard; Byron	Santa Cruz	CA		
Takeda; Genyo	Hirakata			JP
Kato; Shuhei	Otsu			JP

US-CL-CURRENT: 345/503; 345/520

| Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw D |

☐ 17.   Document ID: US 6326964 B1

L9: Entry 17 of 42   File: USPT   Dec 4, 2001

US-PAT-NO: 6326964
DOCUMENT-IDENTIFIER: US 6326964 B1

TITLE: Method for sorting 3D object geometry among image chunks for rendering in a
layered graphics rendering system

DATE-ISSUED: December 4, 2001

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Snyder; John M.	Redmond	WA		
Elliott; Conal M.	Redmond	WA		

US-CL-CURRENT: 345/419

| Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw D |

   ☐   18.   Document ID: US 6292194 B1

L9: Entry 18 of 42   File: USPT   Sep 18, 2001

US-PAT-NO: 6292194
DOCUMENT-IDENTIFIER: US 6292194 B1

TITLE: Image compression method to reduce pixel and <u>texture memory</u> requirements in
graphics applications

DATE-ISSUED: September 18, 2001

INVENTOR-INFORMATION:
NAME   CITY   STATE   ZIP CODE   COUNTRY
Powell, III; William Chambers   Seattle   WA

US-CL-CURRENT: <u>345/582</u>

| Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw D |

   ☐   19.   Document ID: US 6275235 B1

L9: Entry 19 of 42   File: USPT   Aug 14, 2001

US-PAT-NO: 6275235
DOCUMENT-IDENTIFIER: US 6275235 B1

TITLE: High precision texture wrapping method and device

DATE-ISSUED: August 14, 2001

INVENTOR-INFORMATION:
NAME   CITY   STATE   ZIP CODE   COUNTRY
Morgan, III; David L.   Mountain View   CA

US-CL-CURRENT: <u>345/582</u>

| Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw D |

   ☐   20.   Document ID: US 6268875 B1

L9: Entry 20 of 42   File: USPT   Jul 31, 2001

US-PAT-NO: 6268875
DOCUMENT-IDENTIFIER: US 6268875 B1

TITLE: Deferred shading graphics pipeline processor

DATE-ISSUED: July 31, 2001

INVENTOR-INFORMATION:

```
NAME                          CITY              STATE   ZIP CODE   COUNTRY
Duluk, Jr.; Jerome F.         Palo Alto         CA
Hessel; Richard E.            Pleasanton        CA
Arnold; Vaughn T.             Scotts Valley     CA
Benkual; Jack                 Cupertino         CA
Bratt; Joseph P.              San Jose          CA
Cuan; George                  Sunnyvale         CA
Dodgen; Stephen L.            Boulder Creek     CA
Fang; Emerson S.              Fremont           CA
Gong; Zhaoyu                  Cupertino         CA
Ho; Thomas Y.                 Fremont           CA
Hsu; Hengwei                  Fremont           CA
Li; Sidong                    San Jose          CA
Ng; Sam                       Fremont           CA
Papakipos; Matthew N.         Menlo Park        CA
Redgrave; Jason R.            Mountain View     CA
Trivedi; Sushma S.            Sunnyvale         CA
Tuck; Nathan D.               San Diego         CA
```

US-CL-CURRENT: 345/506; 345/422

| Full | Title | Citation | Front | Review | Classification | Date | Reference | | | Claims | KWIC | Draw De |

| Clear | Generate Collection | Print | Fwd Refs | Bkwd Refs | Generate OACS |

Terms	Documents
L8 and texture memory	42

Display Format: | - | Change Format |

Previous Page   Next Page   Go to Doc#

L Number	Hits	Search Text	DB	Time stamp
1	2	(("6504542") or ("0599196")).PN.	USPAT	2004/01/20 12:09
2	2	(("6504542") or ("5999196")).PN.	USPAT	2004/01/20 12:13
3	0	("float$3adjpointadjformat").PN.	USPAT	2004/01/20 12:13
4	904	float$3 adj point adj format	USPAT	2004/01/20 12:14
5	45	(float$3 adj point adj format) same raster$9	USPAT	2004/01/20 12:14

C:\APPS\east\workspaces\10047733.wsp

	U	1	Document ID	Issue Date	Pages	Title	Current OR
1	☐	☐	US 6664962 B1	20031216	37	Shadow mapping in a low cost graphics system	345/426
2	☐	☐	US 6664958 B1	20031216	32	Z-texturing	345/422
3	☐	☐	US 6650331 B2	20031118	64	System, method and computer program product for performing a scissor operation in a graphics processing framework embodied on a single semiconductor platform	345/522
4	☐	☐	US 6650330 B2	20031118	63	Graphics system and method for processing multiple independent execution threads	345/506
5	☐	☐	US 6650327 B1	20031118	17	Display system having floating point rasterization and floating point framebuffering	
6	☐	☐	US 6650325 B1	20031118	63	Method, apparatus and article of manufacture for boustrophedonic rasterization	345/426
7	☐	☐	US 6643744 B1	20031104	27	Method and apparatus for pre-fetching audio data	711/137
8	☐	☐	US 6639595 B1	20031028	46	Achromatic lighting in a graphics system and method	345/426
9	☐	☐	US 6636214 B1	20031021	38	Method and apparatus for dynamically reconfiguring the order of hidden surface processing based on rendering mode	345/422
10	☐	☐	US 6621501 B2	20030916	17	Pixel zoom system and method for a computer graphics system	345/660

	Current XRef	Retrieval Classif	Inventor	S	C	P	2	3	4	5
1	345/421; 345/422; 345/582; 345/589		Komsthoeft, Matthew et al.	☒	☐	☐	☐	☐	☐	☐
2	345/421; 345/428; 345/581; 345/582; 345/587		Leather, Mark M. et al.	☒	☐	☐	☐	☐	☐	☐
3			Lindholm, John Erik et al.	☒	☐	☐	☐	☐	☐	☐
4	345/561; 712/206; 712/215; 712/245; 712/246		Lindholm, John Erik et al.	☒	☐	☐	☐	☐	☐	☐
5	345/422; 365/189.05		Airey, John M. et al.	☒	☐	☐	☐	☐	☐	☐
6	345/422; 345/428; 345/552; 345/561; 345/581; 345/586		Voorhies, Douglas A. et al.	☒	☐	☐	☐	☐	☐	☐
7	711/169; 711/213; 711/218; 712/210		Cheng, Howard H.	☒	☐	☐	☐	☐	☐	☐
8	345/420; 345/520; 345/582; 345/589		Drebin, Robert A. et al.	☒	☐	☐	☐	☐	☐	☐
9	345/421; 345/502; 345/506; 345/532; 345/536; 345/582		Leather, Mark M. et al.	☒	☐	☐	☐	☐	☐	☐
10	345/667		Morrison, Teresa M	☒	☐	☐	☐	☐	☐	☐

	Image Doc. Displayed	PT
1	US 6664962	☐
2	US 6664958	☐
3	US 6650331	☐
4	US 6650330	☐
5	US 6650327	☐
6	US 6650325	☐
7	US 6643744	☐
8	US 6639595	☐
9	US 6636214	☐
10	US 6621501	☐

	U	1	Document ID	Issue Date	Pages	Title	Current OR
11	☐	☐	US 6618048 B1	20030909	26	3D graphics rendering system for performing Z value clamping in near-Z range to maximize scene resolution of visually important Z components	345/422
12	☐	☐	US 6609977 B1	20030826	98	External interfaces for a 3D graphics system	463/36
13	☐	☐	US 6606689 B1	20030812	30	Method and apparatus for pre-caching data in audio memory	711/137
14	☐	☐	US 6597356 B1	20030722	94	Integrated tessellator in a graphics processing unit	345/423
15	☐	☐	US 6580430 B1	20030617	25	Method and apparatus for providing improved fog effects in a graphics system	345/473
16	☐	☐	US 6577309 B2	20030610	64	System and method for a graphics processing framework embodied utilizing a single semiconductor platform	345/426
17	☐	☐	US 6573900 B1	20030603	62	Method, apparatus and article of manufacture for a sequencer in a transform/lighting module capable of processing multiple independent execution threads	345/537
18	☐	☐	US 6515671 B1	20030204	64	Method, apparatus and article of manufacture for a vertex attribute buffer in a graphics processor	345/506
19	☐	☐	US 6504542 B1	20030107	64	Method, apparatus and article of manufacture for area rasterization using sense points	345/441
20	☐	☐	US 6502115 B2	20021231	36	Conversion between packed floating point data and packed 32-bit integer data in different architectural registers	708/204
21	☐	☐	US 6489963 B2	20021203	41	Application program interface for a graphics system	345/522

	Current XRef	Retrieval Classif	Inventor	S	C	P	2	3	4	5
11	345/421; 345/427; 345/428; 345/506; 345/620; 345/627		Leather, Mark M.	☒	☐	☐	☐	☐	☐	☐
12	710/72		Shimizu, Dan et al.	☒	☐	☐	☐	☐	☐	☐
13	712/207		Cheng, Howard H. et al.	☒	☐	☐	☐	☐	☐	☐
14	345/421; 345/426; 345/427		Moreton, Henry P. et al.	☒	☐	☐	☐	☐	☐	☐
15	345/419; 345/426		Hollis, Martin et al.	☒	☐	☐	☐	☐	☐	☐
16	345/418		Lindholm, John Erik et al.	☒	☐	☐	☐	☐	☐	☐
17	345/531; 345/533; 345/556		Lindholm, John Erik et al.	☒	☐	☐	☐	☐	☐	☐
18	345/522		Lindholm, John Erik et al.	☒	☐	☐	☐	☐	☐	☐
19			Voorhies, Douglas A. et al.	☒	☐	☐	☐	☐	☐	☐
20	712/220		Abdallah, Mohammad A. F. et al.	☒	☐	☐	☐	☐	☐	☐
21	345/581; 712/208; 719/328		Parikh, Vimal et al.	☒	☐	☐	☐	☐	☐	☐

	Image Doc. Displayed	PT
11	US 6618048	☐
12	US 6609977	☐
13	US 6606689	☐
14	US 6597356	☐
15	US 6580430	☐
16	US 6577309	☐
17	US 6573900	☐
18	US 6515671	☐
19	US 6504542	☐
20	US 6502115	☐
21	US 6489963	☐

	U	1	Document ID	Issue Date	Pages	Title	Current OR
22	☐	☐	US 6480868 B2	20021112	34	Conversion from packed floating point data to packed 8-bit integer data in different architectural registers	708/204
23	☐	☐	US 6466218 B2	20021015	40	Graphics system interface	345/522
24	☐	☐	US 6462737 B2	20021008	63	Clipping system and method for a graphics processing framework embodied on a single semiconductor platform	345/426
25	☐	☐	US 6456290 B2	20020924	40	Application program interface for a graphics system	345/522
26	☐	☐	US 6452600 B1	20020917	42	Graphics system interface	345/522
27	☐	☐	US 6452595 B1	20020917	69	Integrated graphics processing unit with antialiasing	345/426
28	☐	☐	US 6437781 B1	20020820	16	Computer graphics system having per pixel fog blending	345/426
29	☐	☐	US 6424348 B1	20020723	40	Application program interface for a graphics system	345/522
30	☐	☐	US 6421063 B1	20020716	17	Pixel zoom system and method for a computer graphics system	345/660
31	☐	☐	US 6421058 B2	20020716	40	Graphics command stream for calling a display object in a graphics system	345/522
32	☐	☐	US 6417851 B1	20020709	65	Method and apparatus for lighting module in a graphics processor	345/426
33	☐	☐	US 6411301 B1	20020625	40	Graphics system interface	345/522

	Current XRef	Retrieval Classif	Inventor	S	C	P	2	3	4	5
22	712/221		Abdallah, Mohammad A.F. et al.	☒	☐	☐	☐	☐	☐	☐
23	345/422; 345/501; 345/503; 345/545; 719/328		Parikh, Vimal et al.	☒	☐	☐	☐	☐	☐	☐
24	345/418		Lindholm, John Erik et al.	☒	☐	☐	☐	☐	☐	☐
25	345/520; 345/559; 345/581		Parikh, Vimal et al.	☒	☐	☐	☐	☐	☐	☐
26	345/559; 345/581; 345/582		Parikh, Vimal et al.	☒	☐	☐	☐	☐	☐	☐
27	345/519; 345/611		Montrym, John S. et al.	☒	☐	☐	☐	☐	☐	☐
28			Tucker, S. Paul et al.	☒	☐	☐	☐	☐	☐	☐
29	345/503; 345/559; 345/567; 345/581; 712/205; 719/328		Parikh, Vimal et al.	☒	☐	☐	☐	☐	☐	☐
30			Morrison, Teresa M	☒	☐	☐	☐	☐	☐	☐
31	345/553; 345/567; 712/208; 719/328		Parikh, Vimal et al.	☒	☐	☐	☐	☐	☐	☐
32	345/501; 345/506		Lindholm, John Erik et al.	☒	☐	☐	☐	☐	☐	☐
33	345/427; 345/567; 712/208; 719/328		Parikh, Vimal et al.	☒	☐	☐	☐	☐	☐	☐

	Image Doc. Displayed	PT
22	US 6480868	☐
23	US 6466218	☐
24	US 6462737	☐
25	US 6456290	☐
26	US 6452600	☐
27	US 6452595	☐
28	US 6437781	☐
29	US 6424348	☐
30	US 6421063	☐
31	US 6421058	☐
32	US 6417851	☐
33	US 6411301	☐

	U	1	Document ID	Issue Date	Pages	Title	Current OR
34	☐	☐	US 6353439 B1	20020305	67	System, method and computer program product for a blending operation in a transform module of a computer graphics pipeline	345/561
35	☐	☐	US 6342888 B1	20020129	64	Graphics processing unit with an integrated fog and blending operation	345/426
36	☐	☐	US 6292815 B1	20010918	36	Data conversion between floating point packed format and integer scalar format	708/204
37	☐	☐	US 6266769 B1	20010724	34	Conversion between packed floating point data and packed 32-bit integer data in different architectural registers	712/221
38	☐	☐	US 6263426 B1	20010717	34	Conversion from packed floating point data to packed 8-bit integer data in different architectural registers	712/229
39	☐	☐	US 6247116 B1	20010612	37	Conversion from packed floating point data to packed 16-bit integer data in different architectural registers	712/221
40	☐	☐	US 6198488 B1	20010306	65	Transform, lighting and rasterization system embodied on a single semiconductor platform	345/426
41	☐	☐	US 6005583 A	19991221	18	Precise gradient calculation system and method for a texture mapping system of a computer graphics system	345/587
42	☐	☐	US 5995122 A	19991130	37	Method and apparatus for parallel conversion of color values from a single precision floating point format to an integer format	345/561
43	☐	☐	US 5960213 A	19990928	19	Dynamically reconfigurable multi-function PCI adapter device	710/2

	Current XRef	Retrieval Classif	Inventor	S	C	P	2	3	4	5
34	345/426; 345/427; 345/506		Lindholm, John Erik et al.	☒	☐	☐	☐	☐	☐	☐
35	345/418		Lindholm, John Erik et al.	☒	☐	☐	☐	☐	☐	☐
36	708/497; 708/551; 712/221		Abdallah, Mohammad A. F. et al.	☒	☐	☐	☐	☐	☐	☐
37	345/563		Abdallah, Mohammad A. F. et al.	☒	☐	☐	☐	☐	☐	☐
38	345/563		Abdallah, Mohammad A. F. et al.	☒	☐	☐	☐	☐	☐	☐
39			Abdallah, Mohammad A. F. et al.	☒	☐	☐	☐	☐	☐	☐
40	345/418; 345/419		Lindholm, John Erik et al.	☒	☐	☐	☐	☐	☐	☐
41	345/427		Morrison, Teresa M.	☒	☐	☐	☐	☐	☐	☐
42	345/505; 345/559; 345/600; 708/204; 708/495; 712/222		Hsieh, Hsien-Cheng E. et al.	☒	☐	☐	☐	☐	☐	☐
43	710/10; 713/1		Wilson, Malcolm Eric	☒	☐	☐	☐	☐	☐	☐

	Image Doc. Displayed	PT
34	US 6353439	☐
35	US 6342888	☐
36	US 6292815	☐
37	US 6266769	☐
38	US 6263426	☐
39	US 6247116	☐
40	US 6198488	☐
41	US 6005583	☐
42	US 5995122	☐
43	US 5960213	☐

	U	1	Document ID	Issue Date	Pages	Title	Current OR
44	☐	☐	US 5862066 A	19990119	10	Methods and apparatus for fast check of floating point zero or negative zero	708/525
45	☐	☐	US 5384912 A	19950124	54	Real time video image processing system	345/501

	Current XRef	Retrieval Classif	Inventor	S	C	P	2	3	4	5
44	708/504		Rossin, Theodore G. et al.	☒	☐	☐	☐	☐	☐	☐
45			Ogrinc, Michael A. et al.	☒	☐	☐	☐	☐	☐	☐

	Image Doc. Displayed	PT
44	US 5862066	☐
45	US 5384912	☐